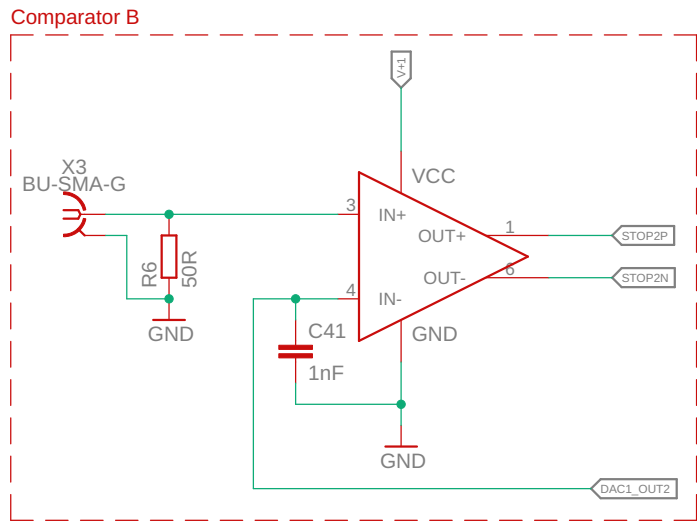
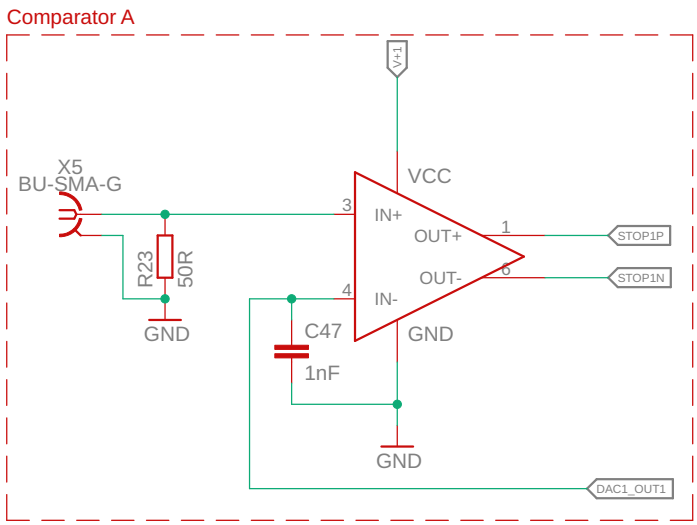
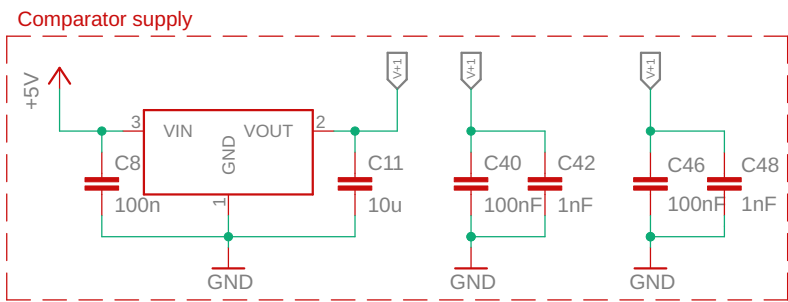


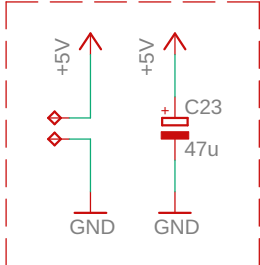
as6501_v2

not saved!

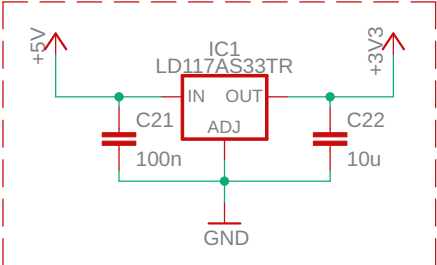
Sheet: 1/1



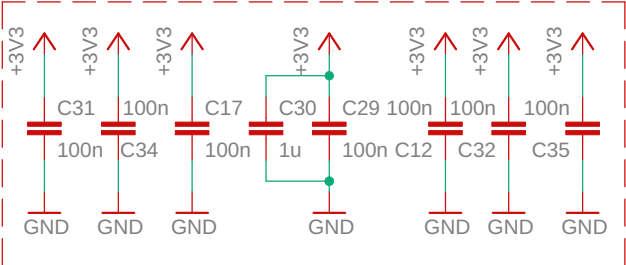
Power input



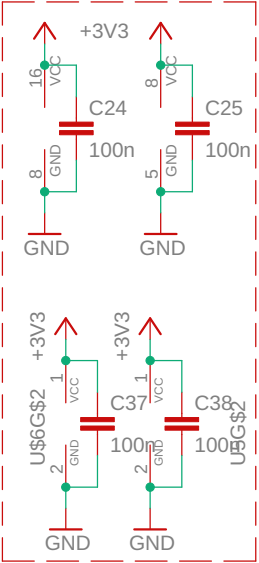
Digital V-reg



MCU decoupling



LVDS <---> CMOS



AS6501

