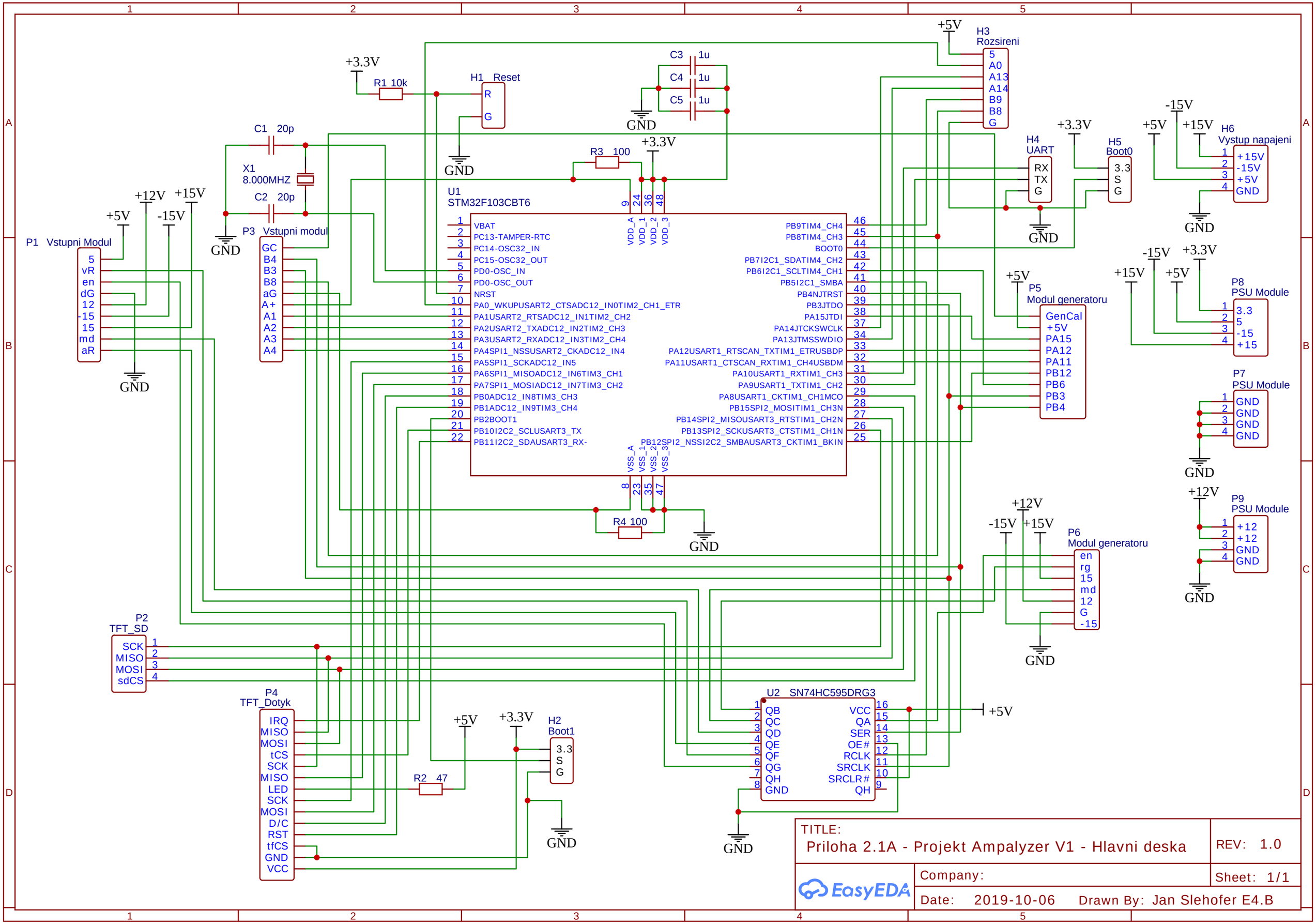
U1
STM32F103CBT6
VBAT
PC13-TAMPER-RTC
PC14-OSC32_IN
PC15-OSC32_OUT
PD0-OSC_IN
PD0-OSC_OUT
NRST
PA0_WKUPUSART2_CTSADC12_IN0TIM2_CH1_ETR
PA1USART2_RTSADC12_IN1TIM2_CH2
PA2USART2_TXADC12_IN2TIM2_CH3
PA3USART2_RXADC12_IN3TIM2_CH4
PA4SPI1_NSSUSART2_CKADC12_IN4
PA5SPI1_SCKADC12_IN5
PA6SPI1_MISOADC12_IN6TIM3_CH1
PA7SPI1_MOSIADC12_IN7TIM3_CH2
PB0ADC12_IN8TIM3_CH3
PB1ADC12_IN9TIM3_CH4
PB2BOOT1
PB10I2C2_SCLUSART3_TX
PB11I2C2_SDAUSART3_RX-
PB12SPI2_NSSI2C2_SMBAUSART3_CKTIM1_BKIN
PB13SPI2_SCKUSART3_CTSTIM1_CH1N
PB14SPI2_MISOUSART3_RTSTIM1_CH2N
PB15SPI2_MOSITIM1_CH3N
PA8USART1_CKTIM1_CH1MCO
PA9USART1_TXTIM1_CH2
PA10USART1_RXTIM1_CH3
PA11USART1_CTSCAN_RXTIM1_CH4USBDM
PA12USART1_RTSCAN_TXTIM1_ETRUSBDP
PA13JTMSSWDIO
PA14JTCKSWCLK
PA15JTDI
PB3JTDO
PB4NJTRST
PB5I2C1_SMBA
PB6I2C1_SCLTIM4_CH1
PB7I2C1_SDATIM4_CH2
BOOT0
PB8TIM4_CH3
PB9TIM4_CH4
VDD_A
VDD_1
VDD_2
VDD_3
VSS_A
VSS_1
VSS_2
VSS_3
H1 Reset
R
G
R1 10k
+3.3V
GND
C1 20p
C2 20p
X1
8.000MHZ
C3 1u
C4 1u
C5 1u
R3 100
R4 100
P1 Vstupni Modul
5
vR
en
dG
12
-15
15
md
aR
+5V
+12V
+15V
-15V
P3 Vstupni modul
GC
B4
B3
B8
aG
A+
A1
A2
A3
A4
H3
Rozsireni
5
A0
A13
A14
B9
B8
G
H4
UART
RX
TX
G
H5
Boot0
3.3
S
G
H6
Vystup napajeni
+15V
-15V
+5V
GND
P5
Modul generatoru
GenCal
+5V
PA15
PA12
PA11
PB12
PB6
PB3
PB4
P6
Modul generatoru
en
rg
15
md
12
G
-15
P7
PSU Module
P8
PSU Module
3.3
5
-15
+15
P9
PSU Module
+12
+12
GND
GND
P2
TFT_SD
SCK
MISO
MOSI
sdCS
P4
TFT_Dotyk
IRQ
MISO
MOSI
tCS
SCK
MISO
LED
SCK
MOSI
D/C
RST
tfCS
GND
VCC
H2
Boot1
R2 47
U2 SN74HC595DRG3
QB
QC
QD
QE
QF
QG
QH
GND
VCC
QA
SER
OE#
RCLK
SRCLK
SRCLR#
QH'
TITLE:
Priloha 2.1A - Projekt Ampalyzer V1 - Hlavni deska
REV: 1.0
Company:
Sheet: 1/1
Date: 2019-10-06
Drawn By: Jan Slehofer E4.B
EasyEDA